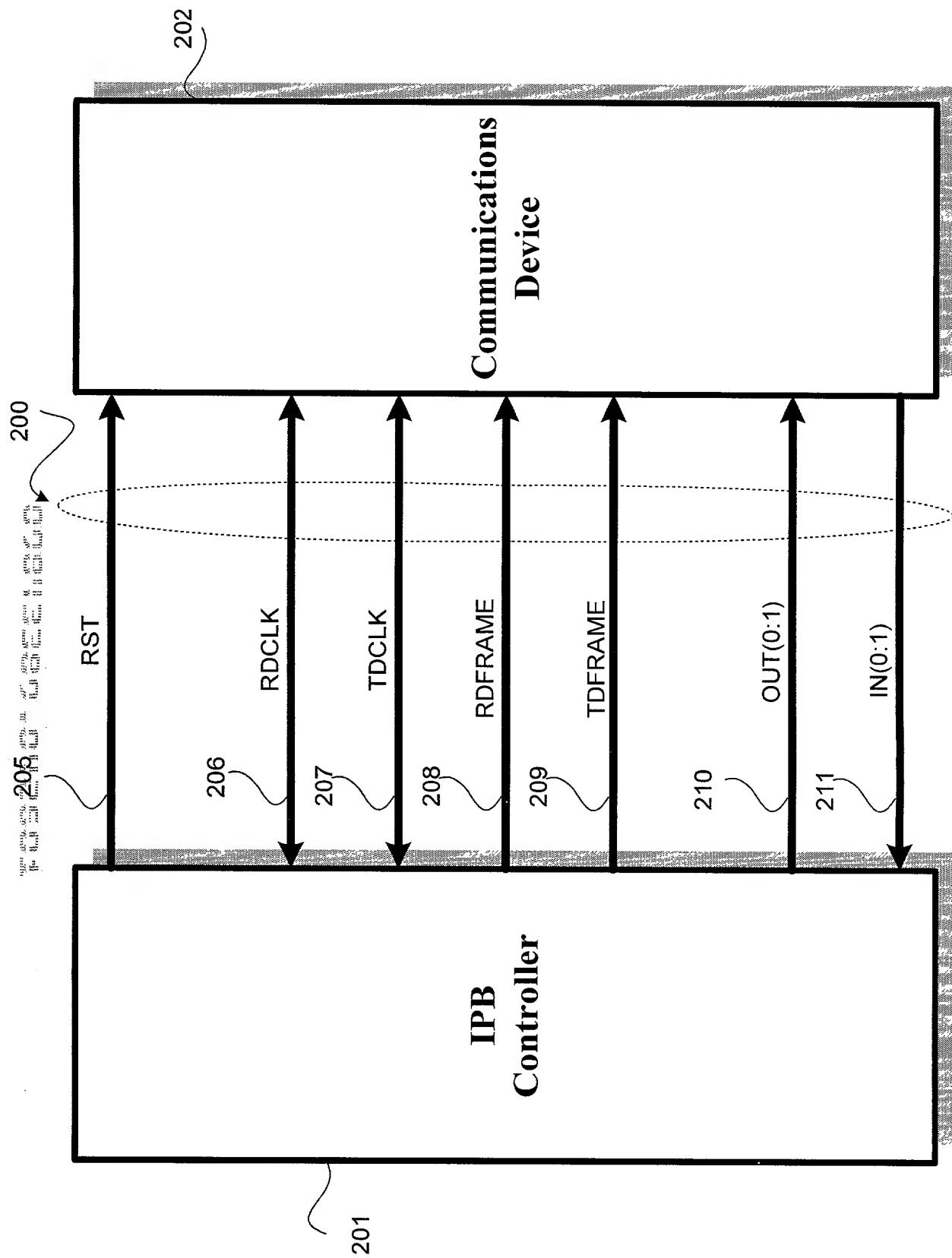


FIG. 1

FIG. 2a



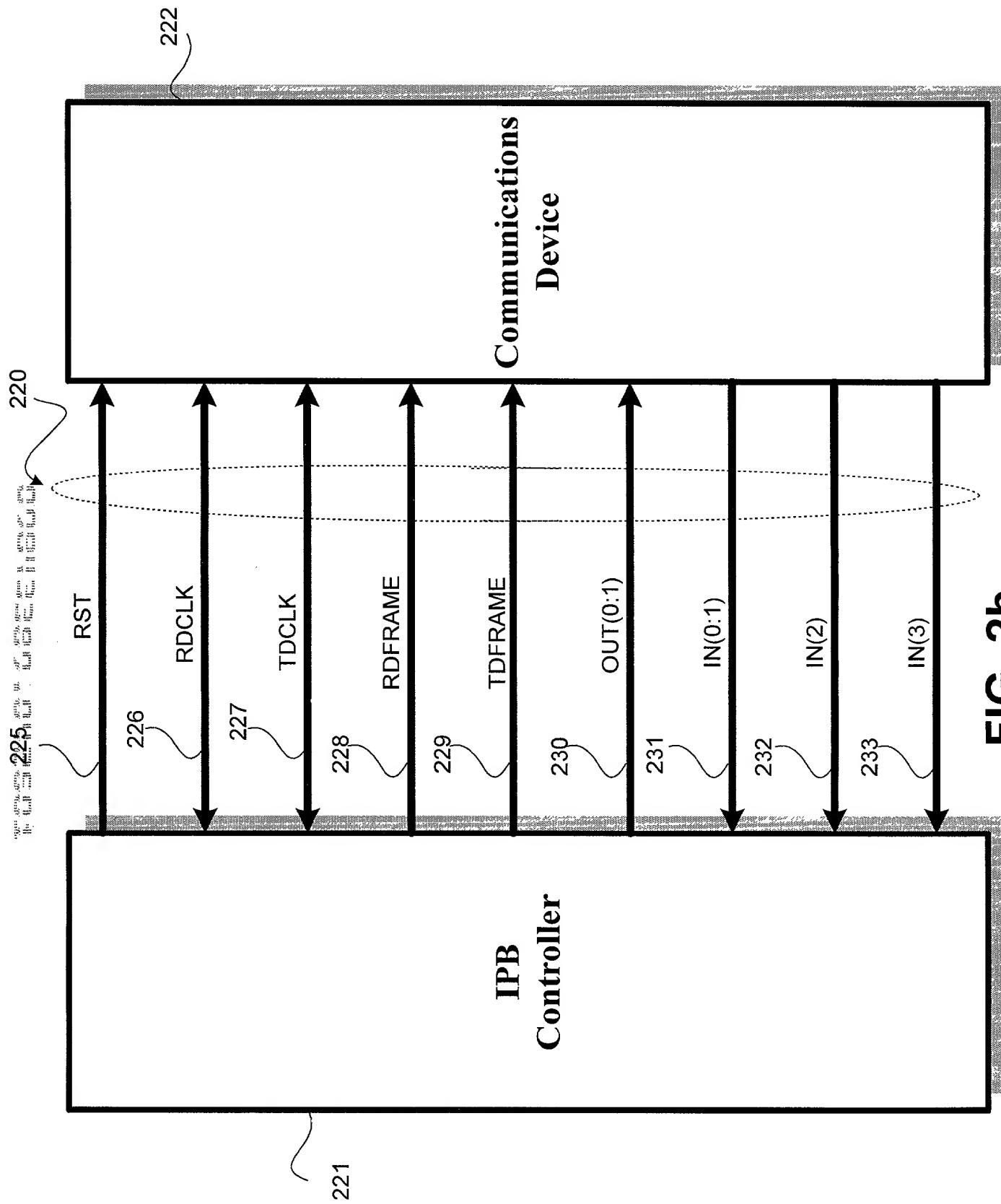


FIG. 2b

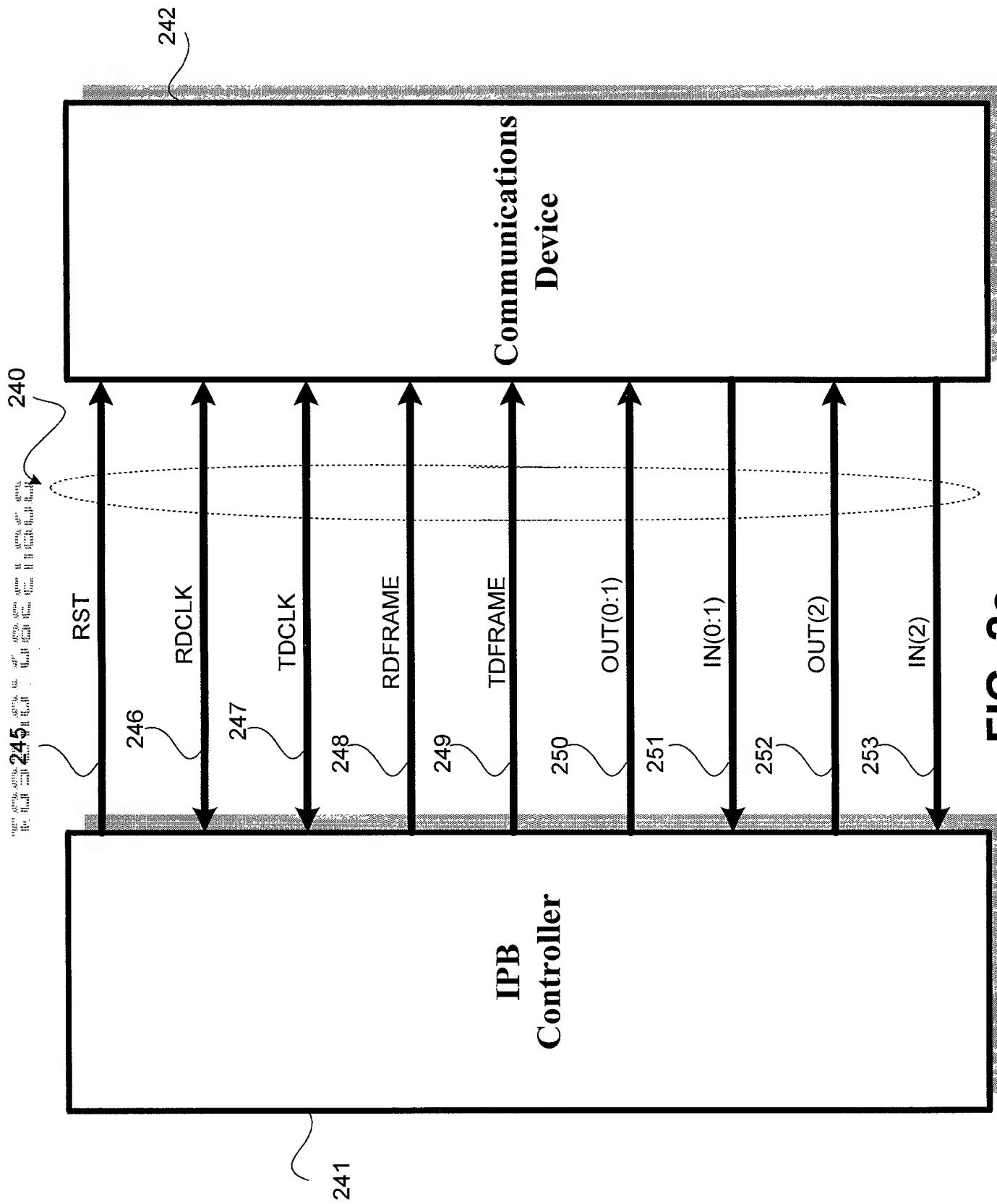


FIG. 2C

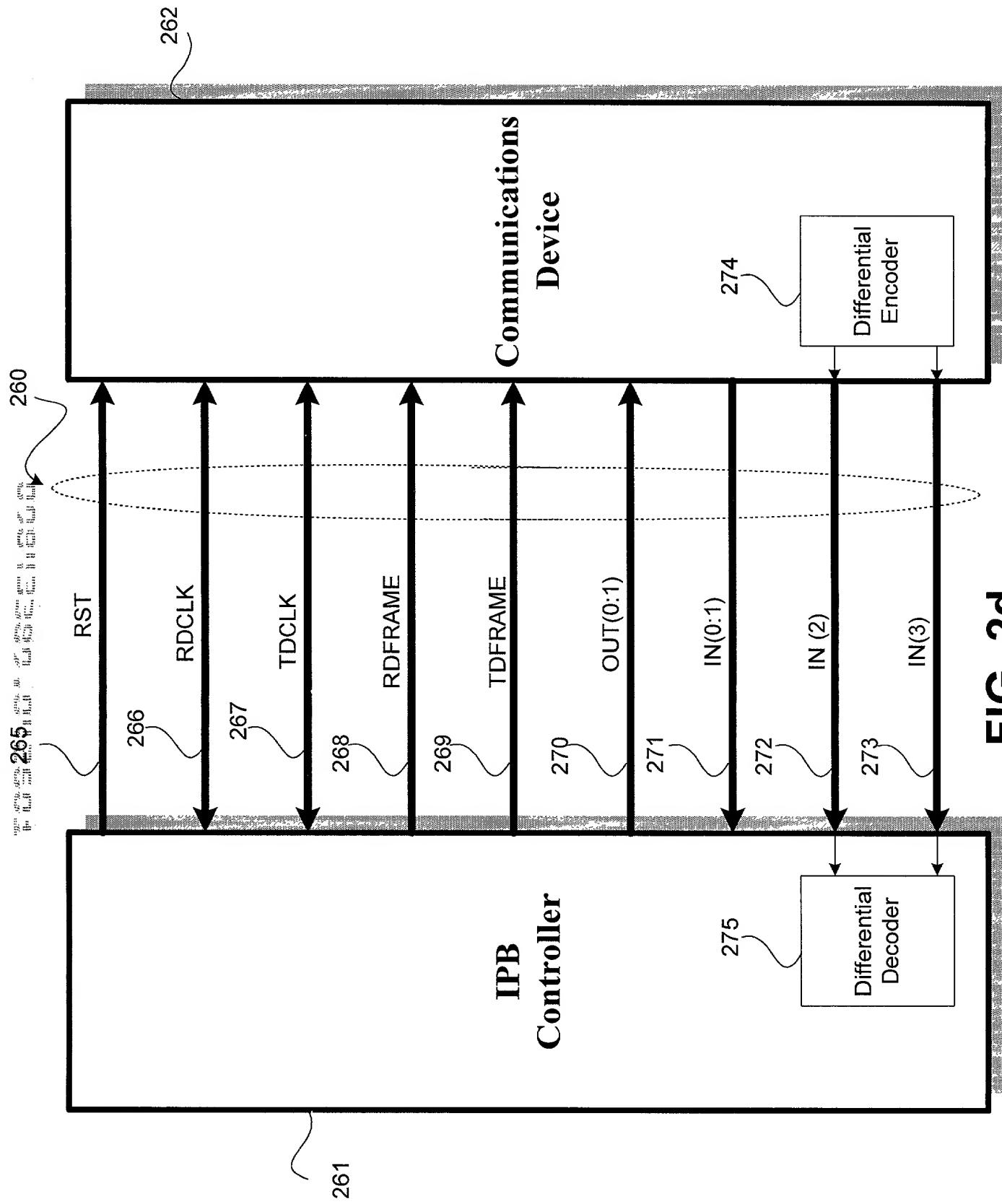


FIG. 2d

rdframe 208
rdframe 209
rdclk 206
tdclk 207
slot 14 bit 0
302
out:0
in:0
out:1
in:1

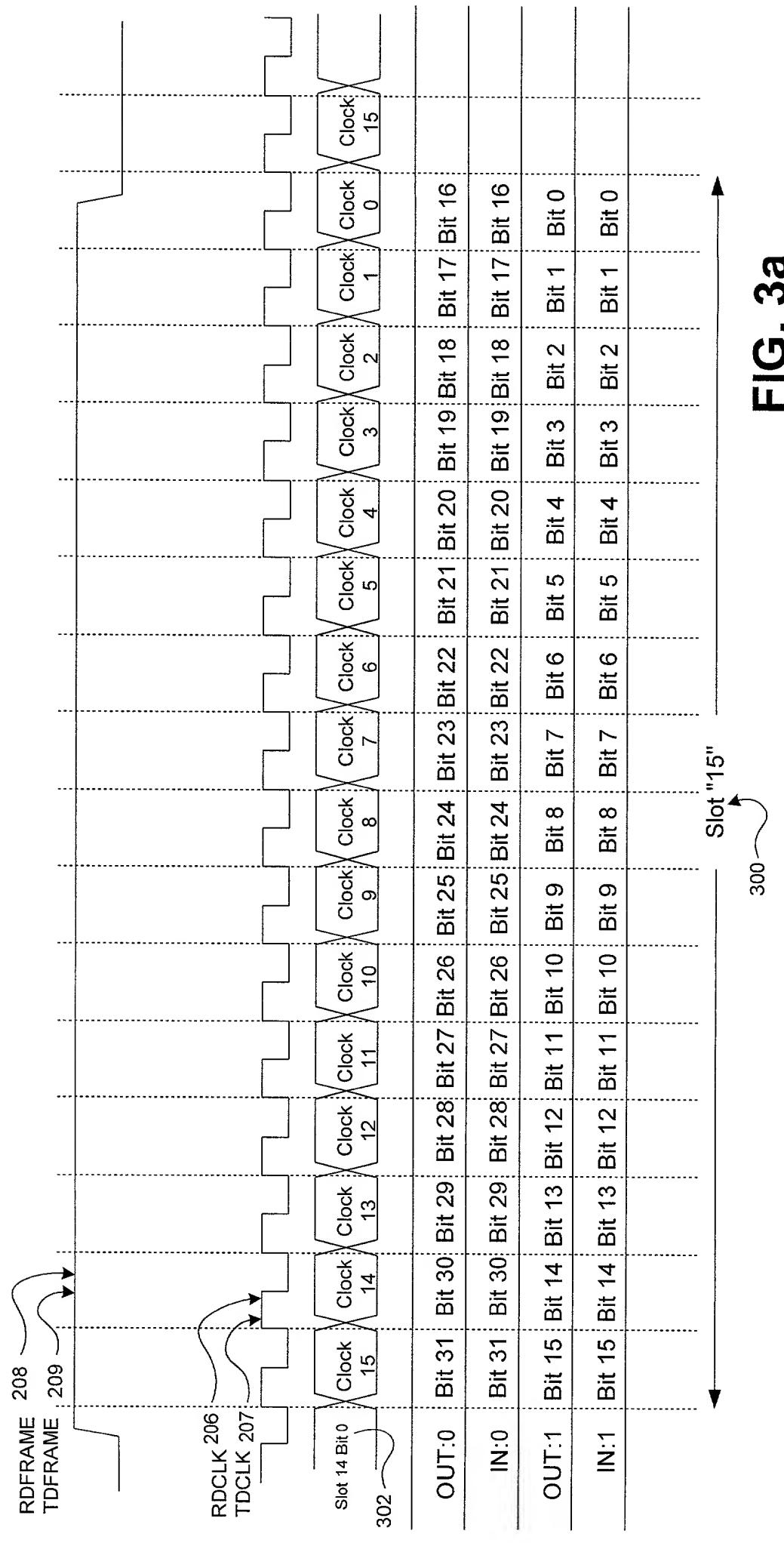
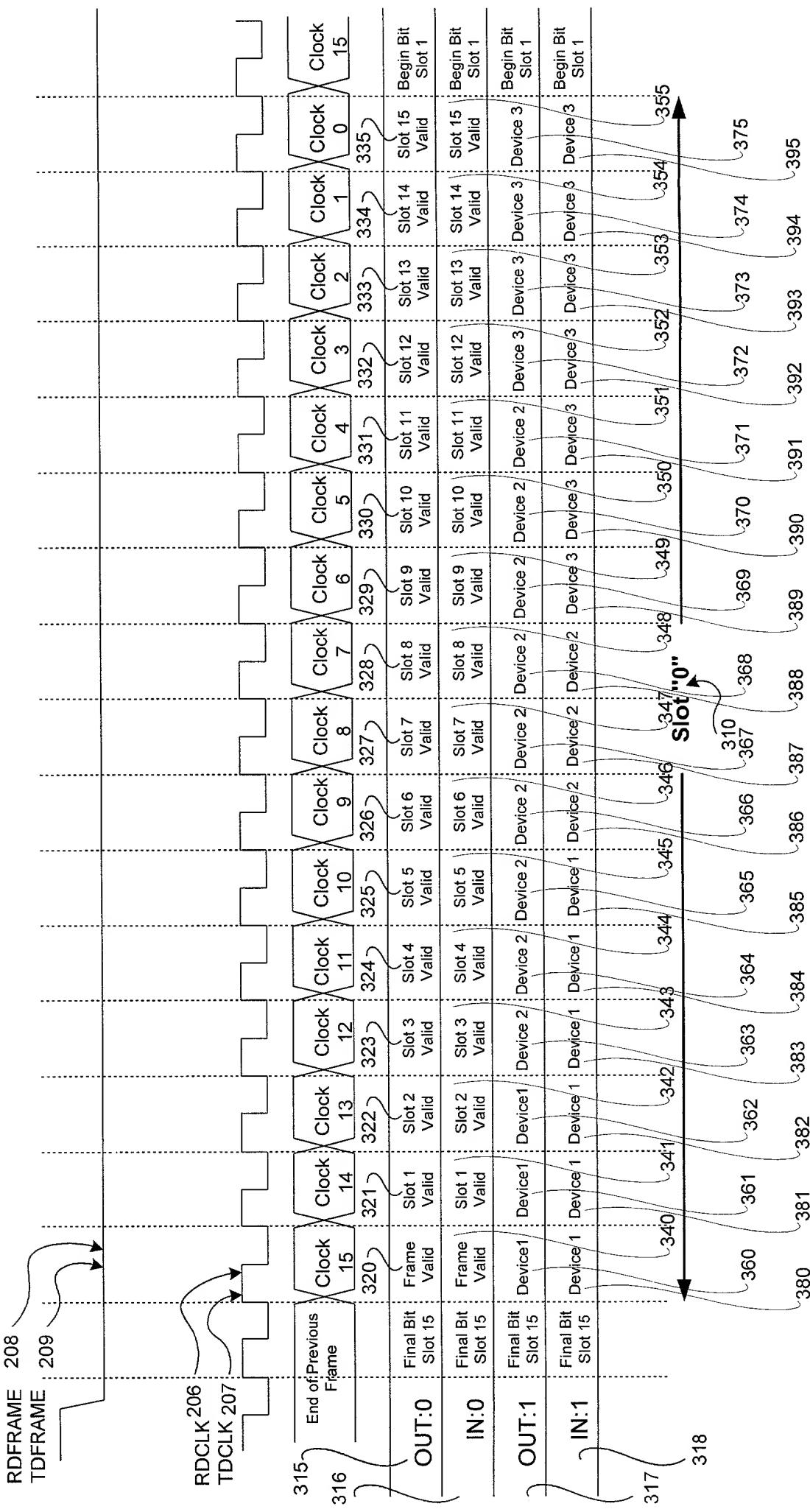


FIG. 3a

FIG. 3b



228 229 226 227 3020 3000 Slot "15"

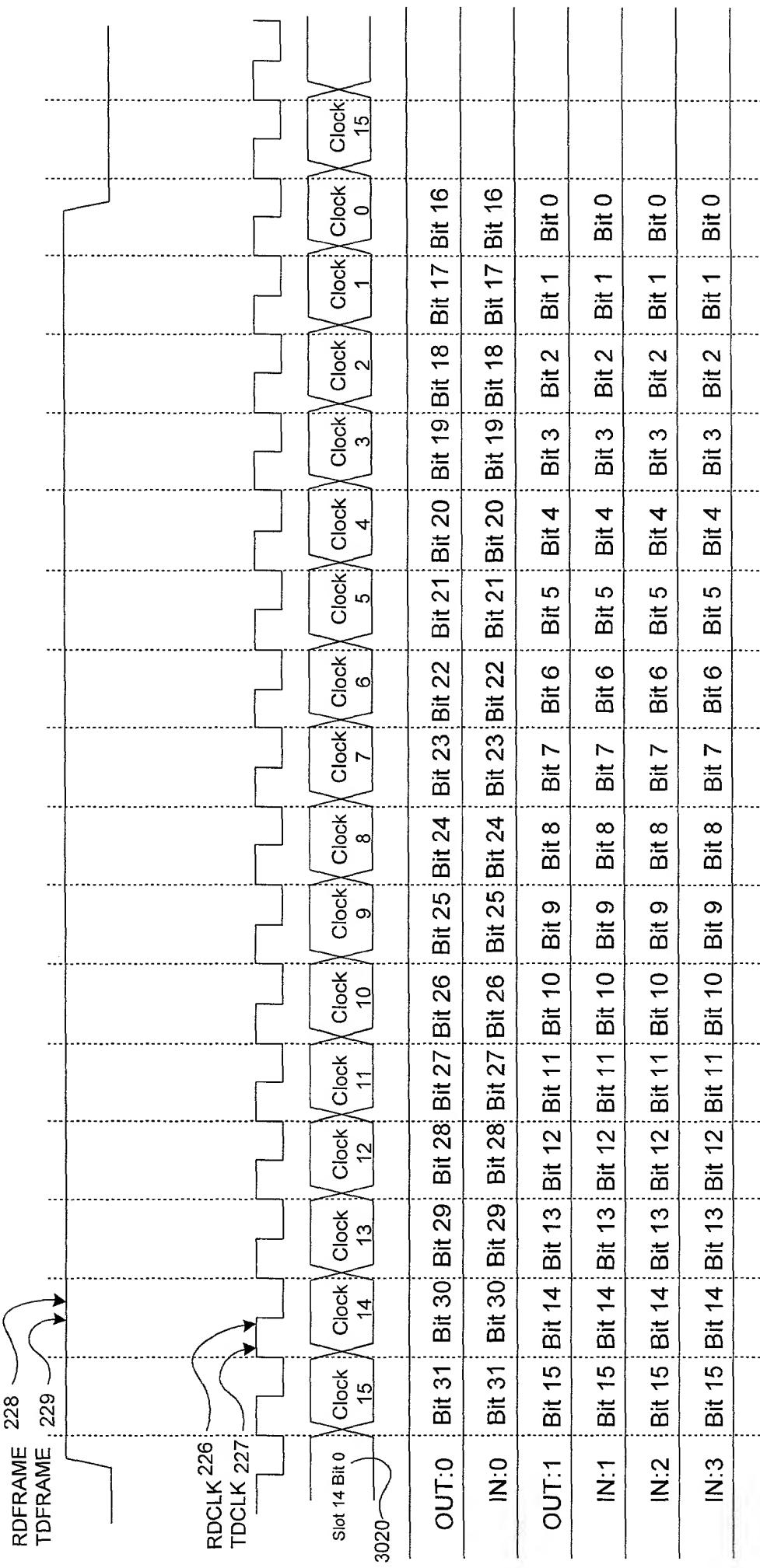


FIG. 3C

FIG. 4

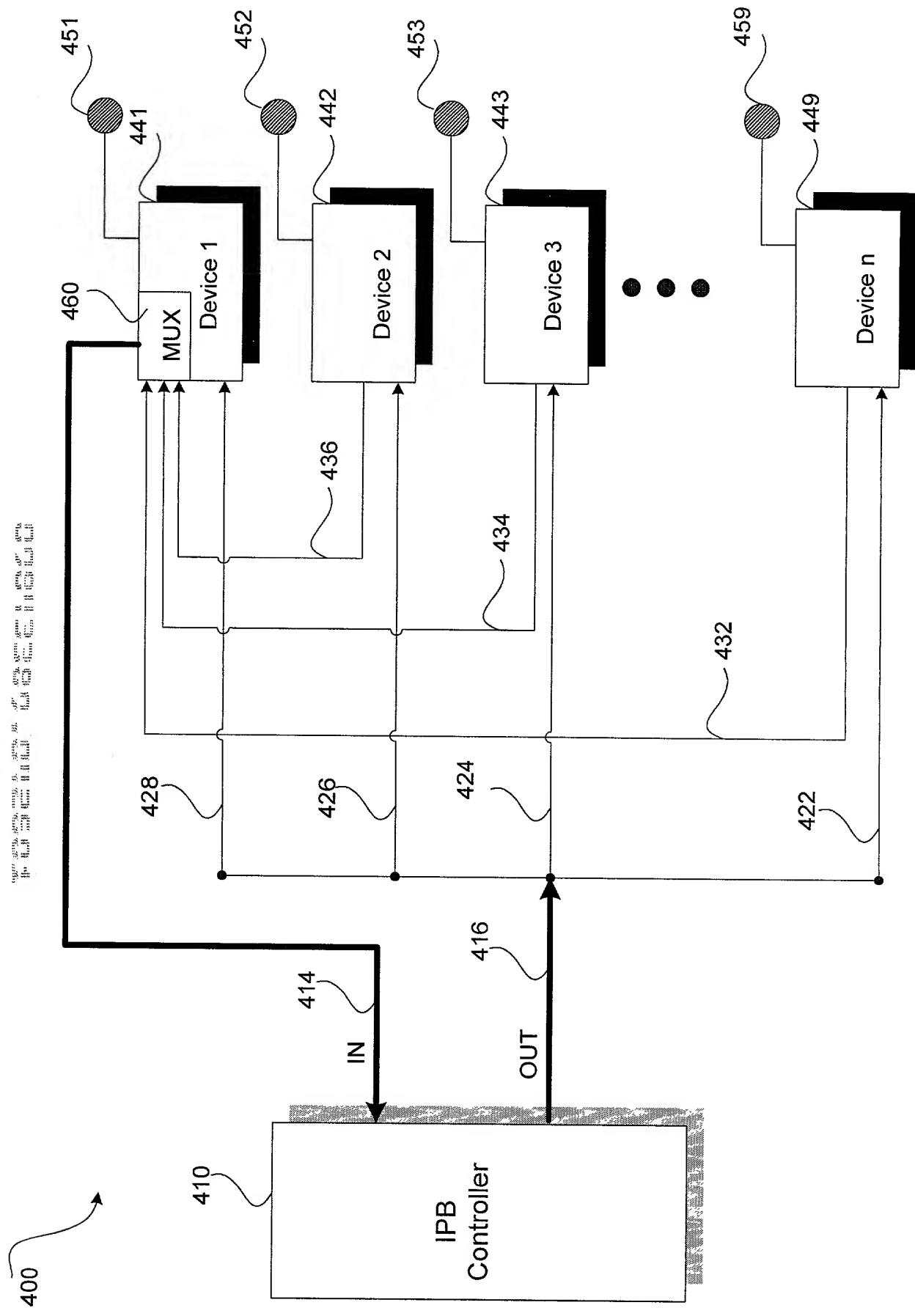


FIG. 5

